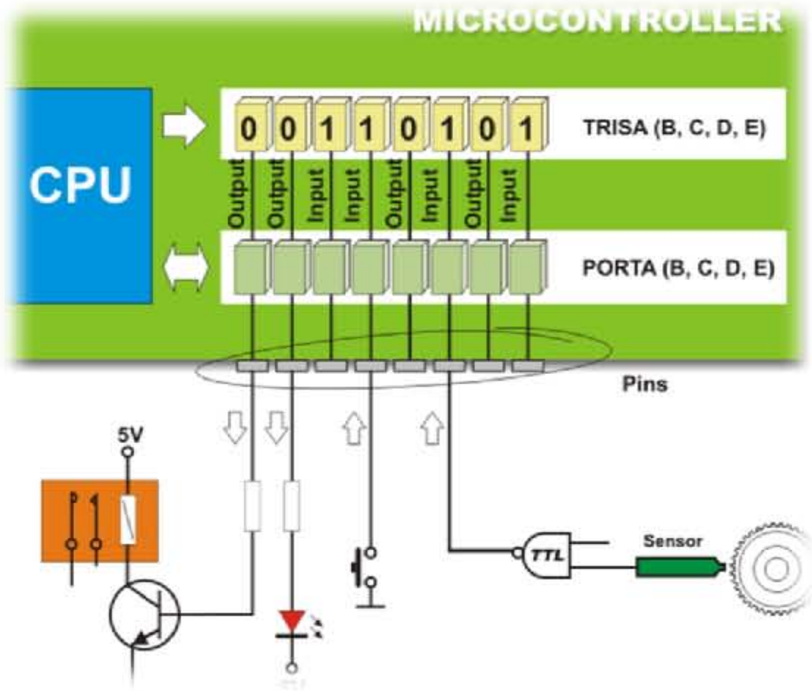
PORTS DE I/O



http://www.mikroe.com/img/publication/pic-books/pic-microcontrollers/chapter/03/fig3-1.gif

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O registro TRISx

- É composto de 8 bits de 0 a 7 e que define o destino de cada Port

Bits	7	6	5	4	3	2	1	0
			1		0			

E só pode ser usado como entrada. Só se reconfigurado.

Estado de um Port
0 = Output
1 = Input
Neste caso o
PORTx = 1 = Input

Aqui é só saída
pois = 0

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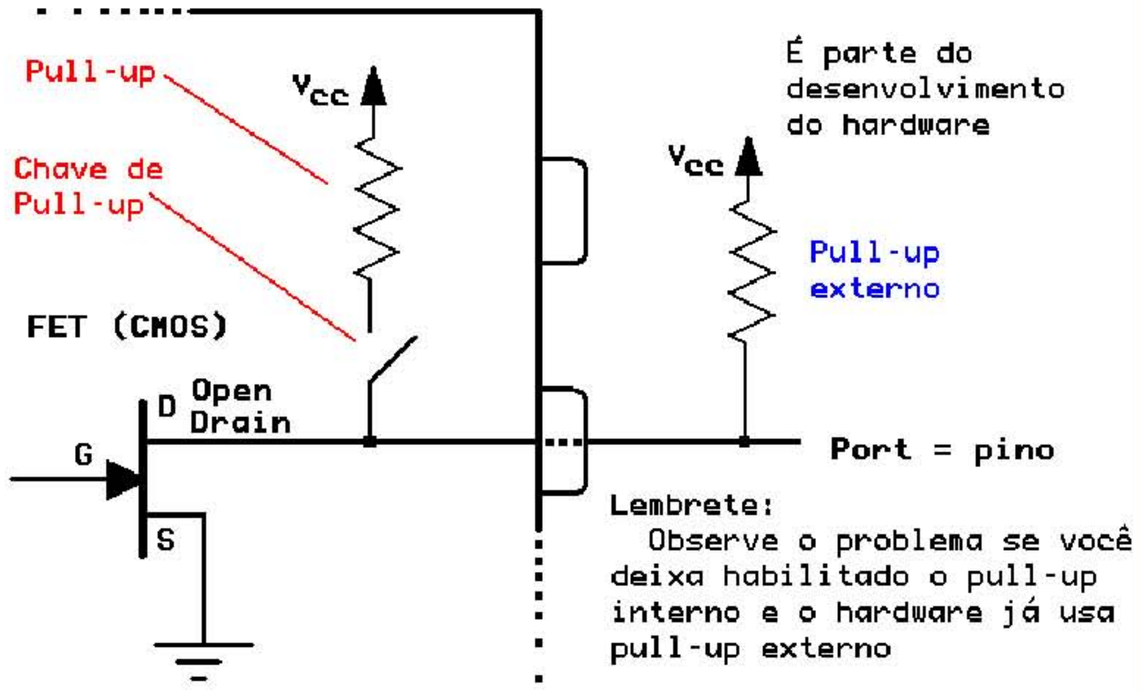
TRISx, PORTx e LATx

- Há um TRISx para cada PORTx e LATx
- No 18F4550:
  - ☒ TRISA até TRISE
  - ☒ PORTA até PORTE
  - ☒ LATA até LATE



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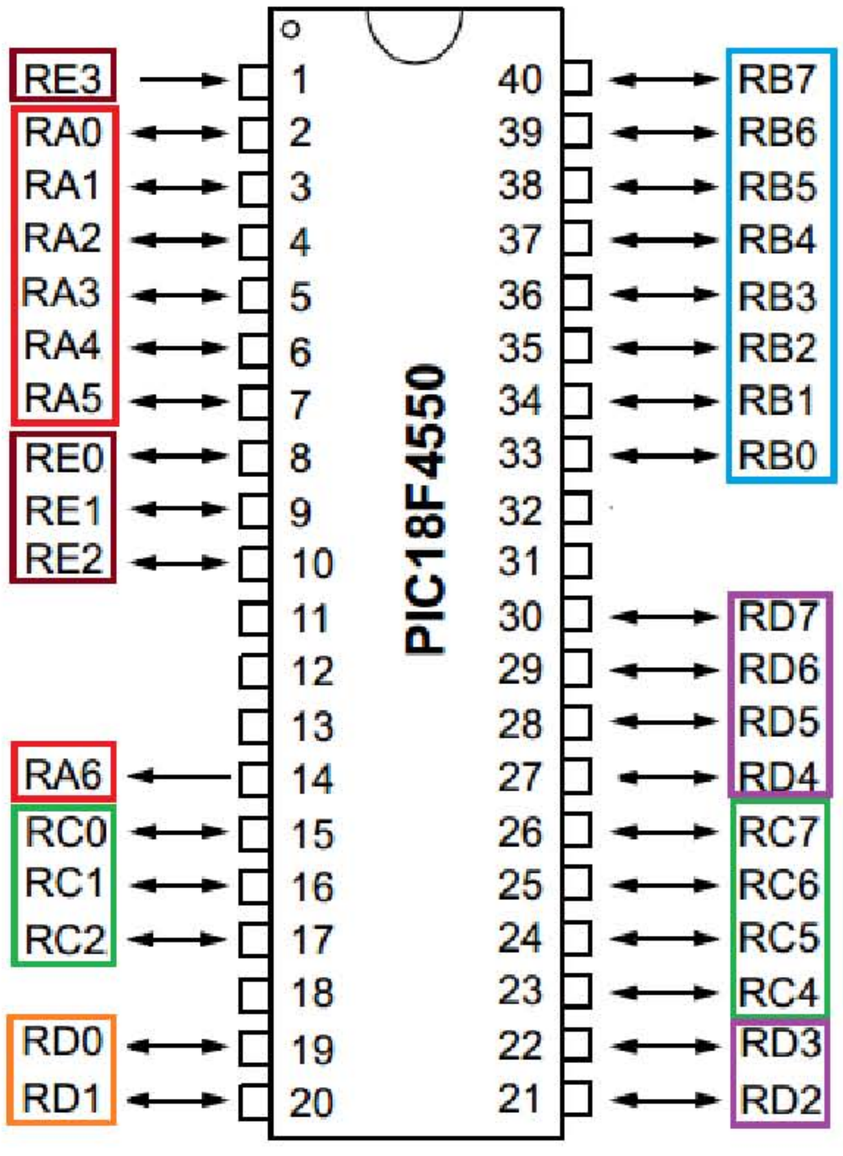
Os Ports de I/O

- PORTA até PORTE



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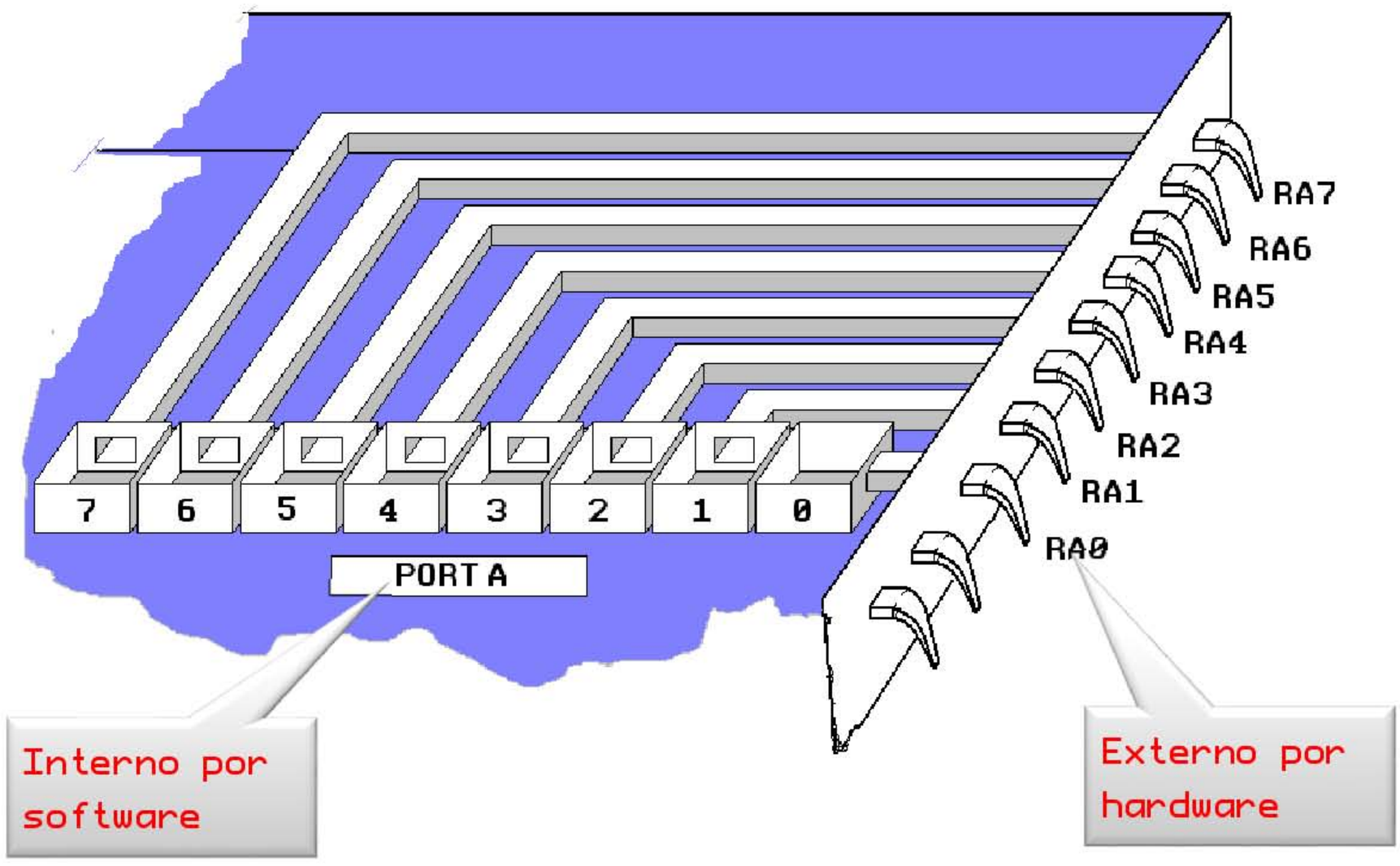
O registro PORTx



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Proposta

- Executar os seguintes comandos:
  - PORTA.RA0 = 1;
  - PORTA.RA1 = 1;
- Fique atento no ciclo:
  - Read-Store-Modify-Write necessário para gravar um bit

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Usando o PORTx

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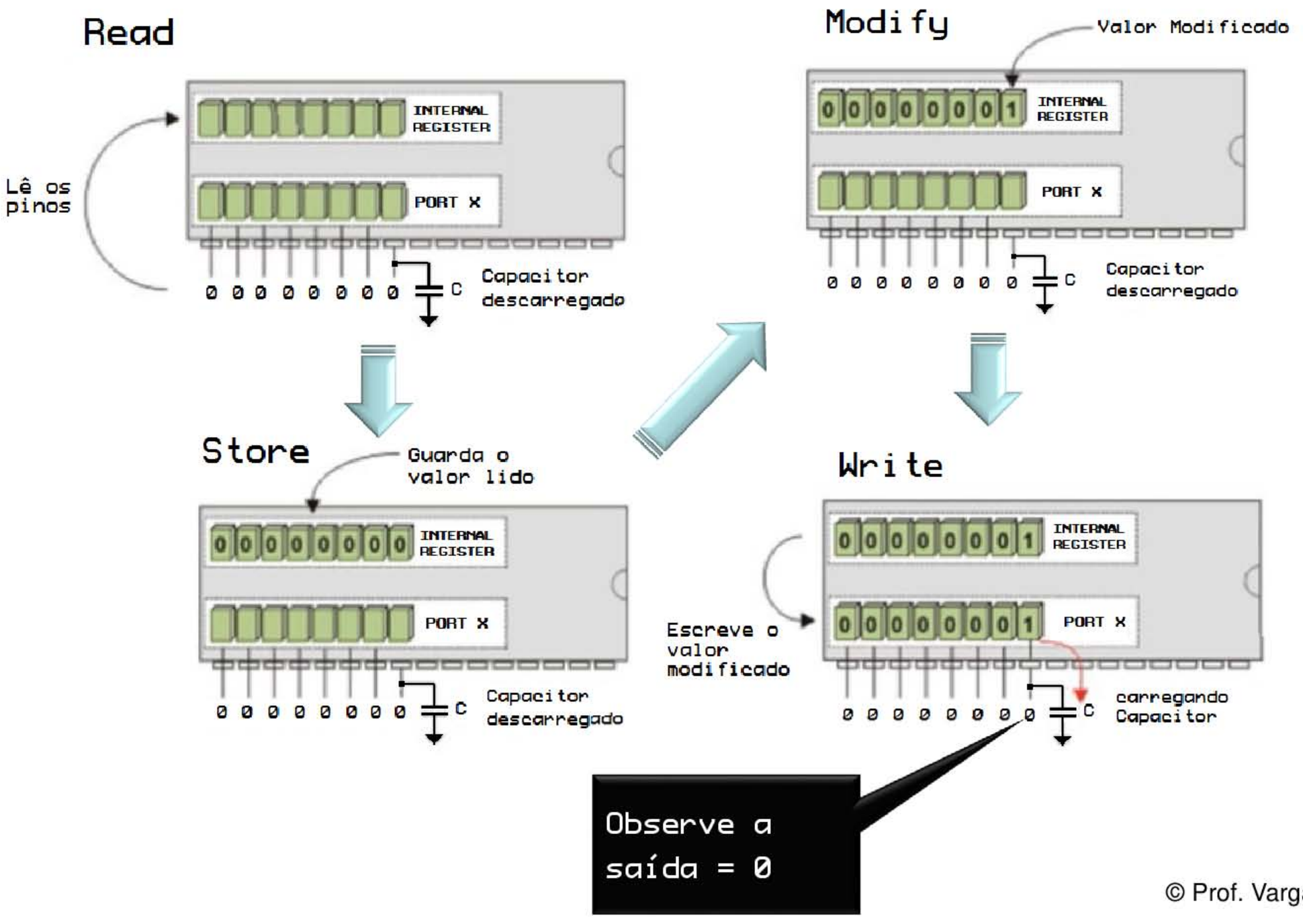
Escrevendo o 1º bit



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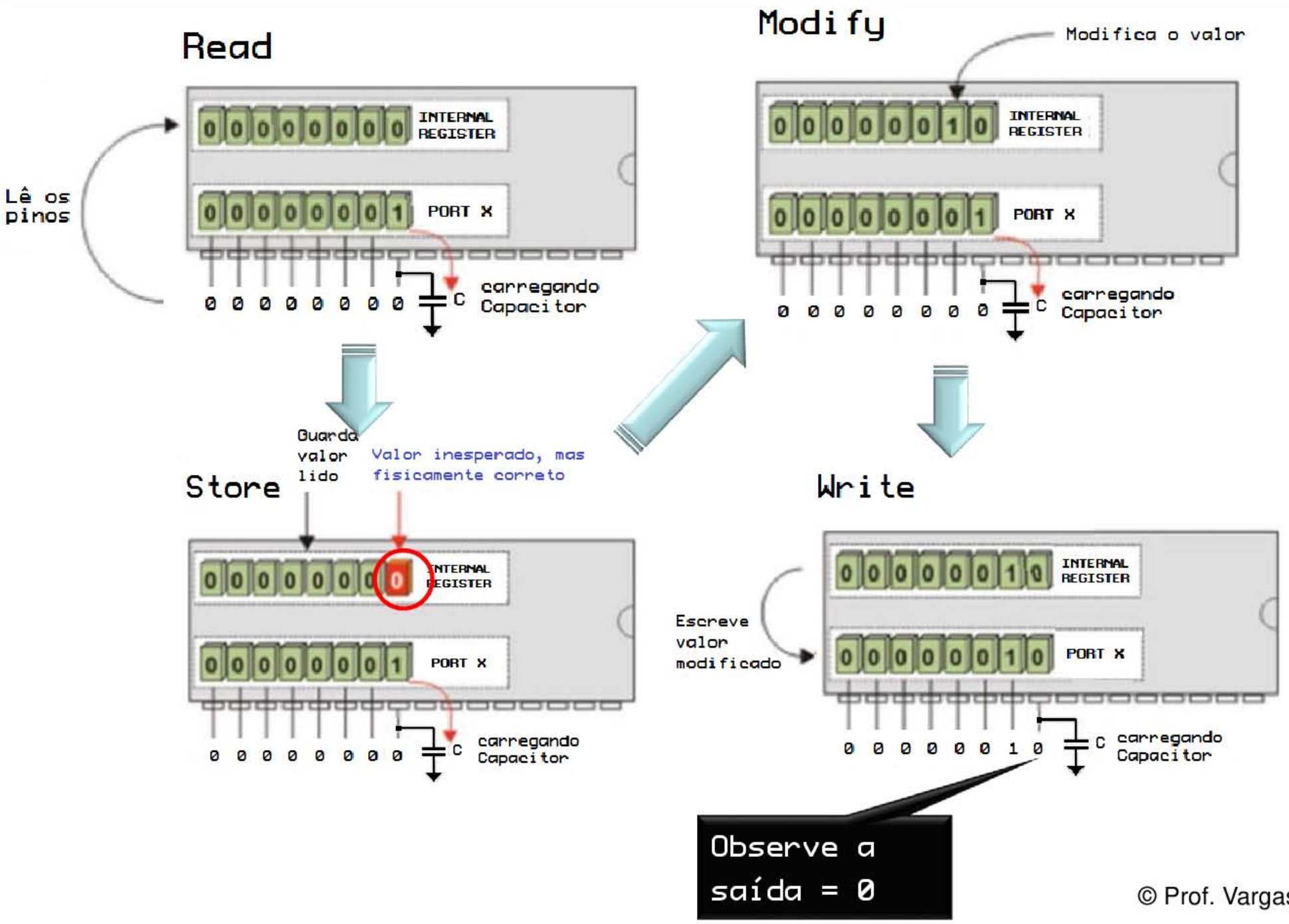
Escrevendo o 2º bit



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Usando o LaTeX

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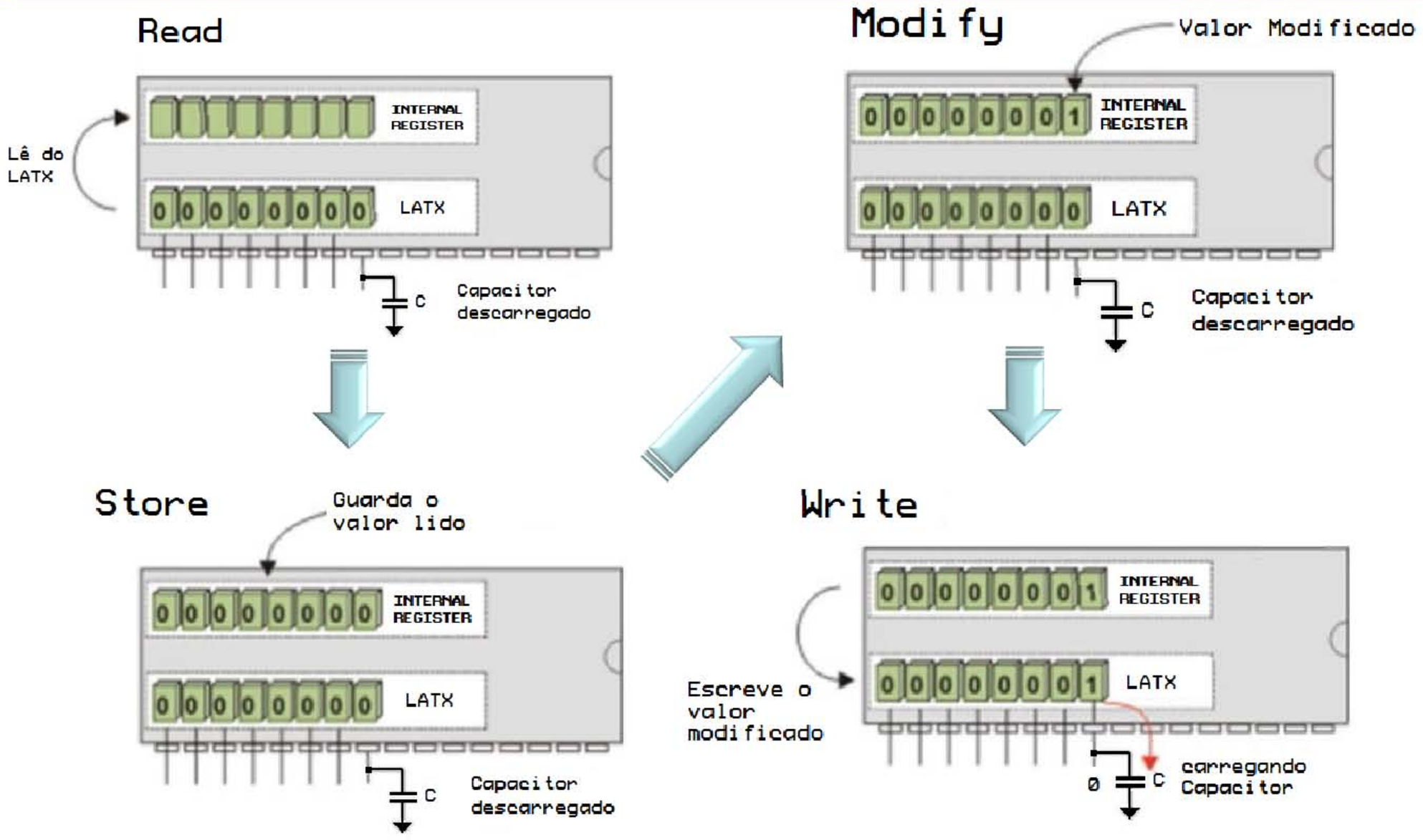
Escrevendo o 1º bit



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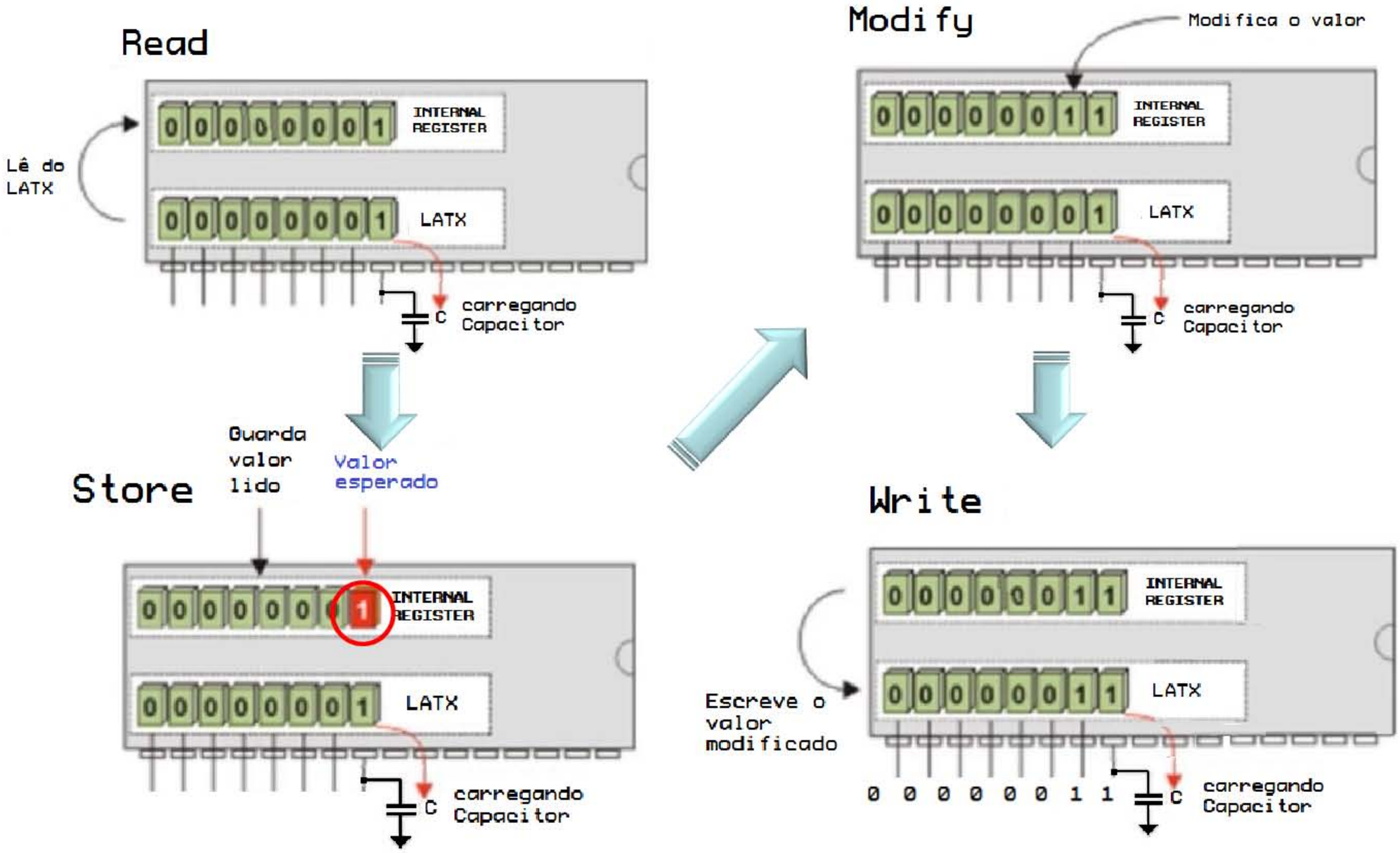
Escrevendo o 2º bit



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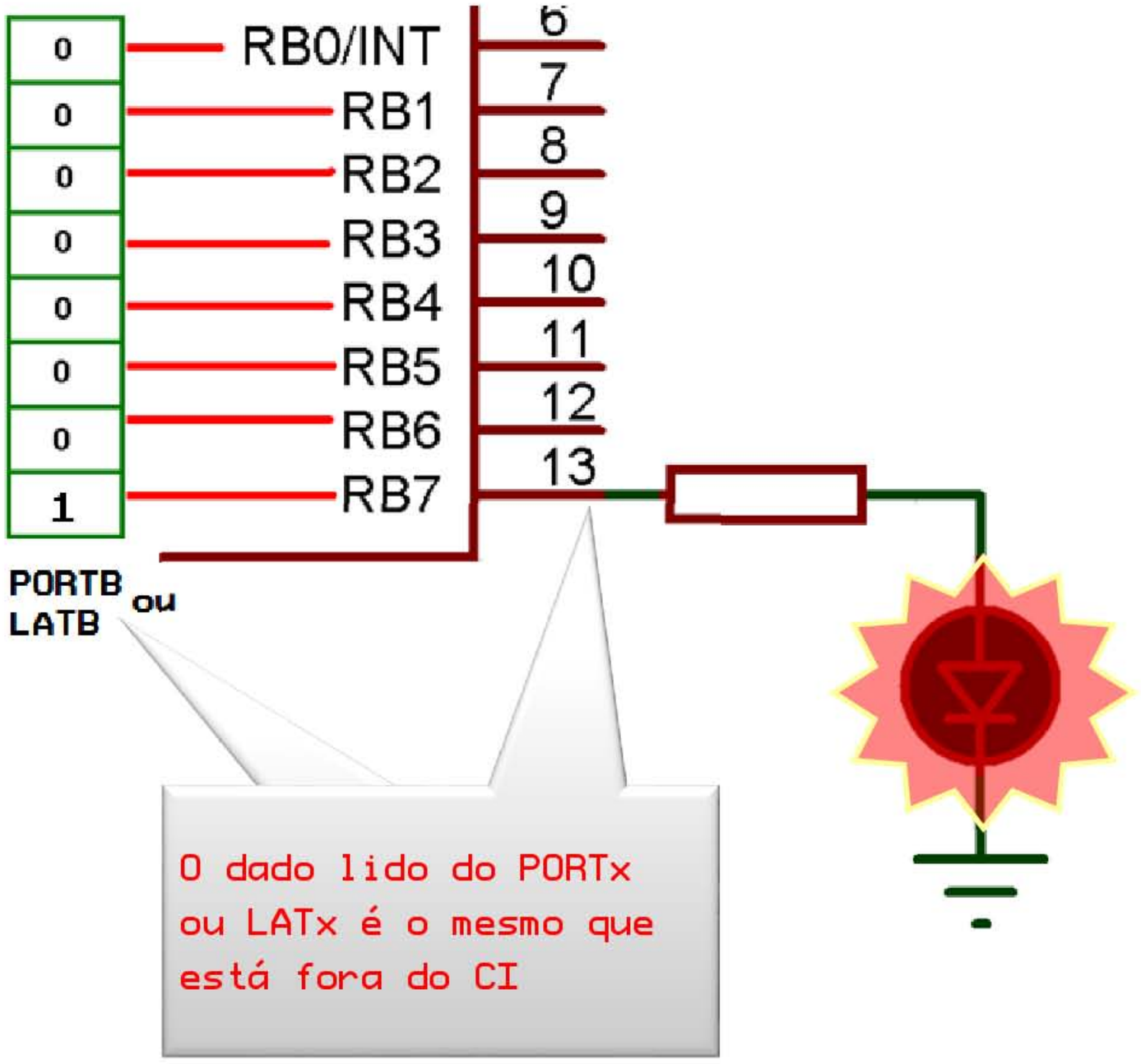
Usando no hardware



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